

Protocol Validation System for On-Board Communications



PVS Project Overview

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Noordwijk, Netherlands*

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- Current **evolution of satellite on-board communications**, require the development & experimentation with new dedicated communication protocols and services (SpW, SOIS, etc.)
- **New generation of validation tools is required** to support advanced protocol development, test, integration & validation

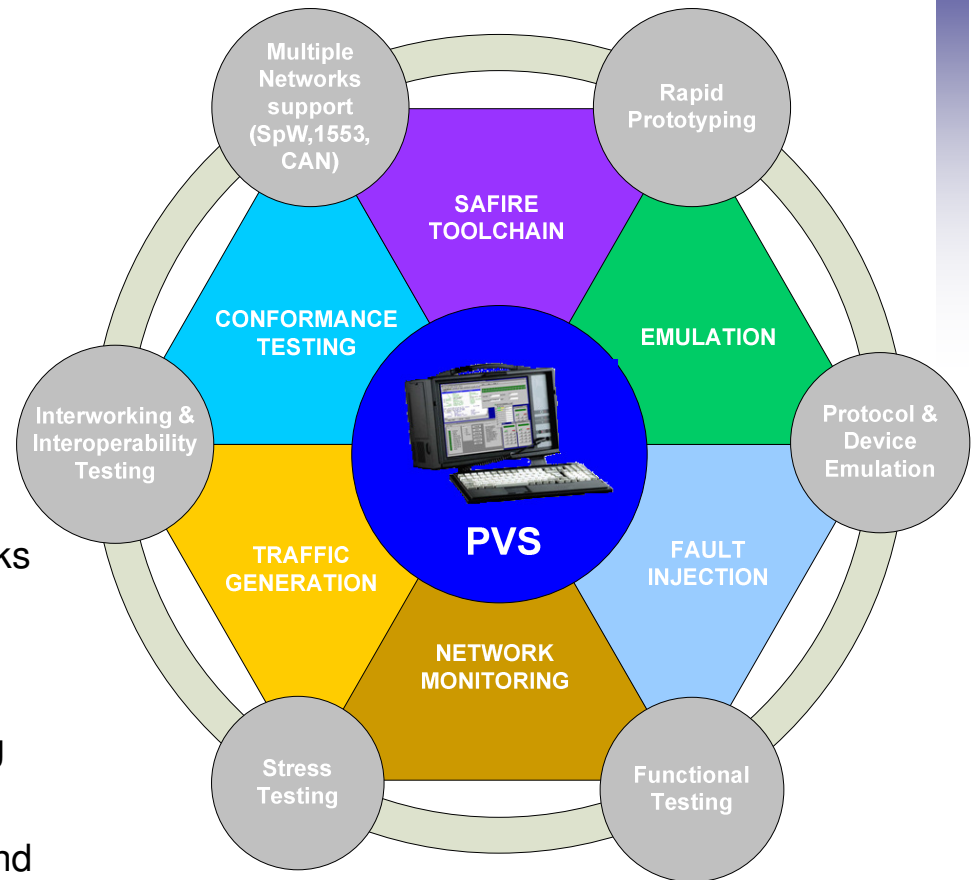


- A **protocol validation tool** with more than 20 years of experience in the **telecommunication sector** & with hundreds of installations worldwide
- Has been widely used for testing various telecommunication networks (ISDN, V5, SS7, IN, GSM, UMTS, VoIP, custom)

The basic motivation is to provide an open, scalable and fully integrated protocol validation system (PVS) for satellite on-board communications supporting multiple physical interfaces (SpW, MIL-STD-1553, CAN) and functionalities (emulation, validation, interworking testing, monitoring).

PVS Features

- **DEVICE EMULATION:** replacement of a network element in the testbed
- **PROTOCOL EMULATION:** experimentation with various protocol features (protocol variables, exclusion/inclusion of protocol optional functions etc.)
- **CONFORMANCE TESTING:** ECSS and CCSDS standards compliance testing.
- **FAULT-INJECTION:** Validation of devices/networks behavior under erroneous conditions
- **TRAFFIC GENERATION:** Bulk traffic injection for performance evaluation and network dimensioning
- **NETWORK MONITORING:** Traffic data capture and logging for post processing and playback



Areas of Use / PVS Instances

The PVS shall cover the needs of:

■ Rapid prototyping

- *R&D and feasibility studies* →
- *System analysis teams* →

■ Functional testing

- *Device providers* →
- *AIV teams* →

■ Interoperability testing

- *Device providers* →
- *AIT teams* →

■ Stress testing

- *All users* →

■ Protocol Analysis

- *All users* →



- HW Board and SAFIRE basic, protocols on request
- Rugged Notebook, SAFIRE basic, pre-selected library of protocols/tests
- Portable (5 slots), SAFIRE basic, pre-selected library of protocols/tests, interworking tests
- Rackmount, SAFIRE basic, pre-selected library of protocols/tests, interworking tests, other tests

Benefits

- All-in-one validation environment
- Suitable for use in many different areas and by many different users
- Easy integration of new physical interfaces, protocol emulators ,3rd party applications
- Support of multiple networks simultaneously for interworking validation scenarios

PVS-Phase 1 Overview

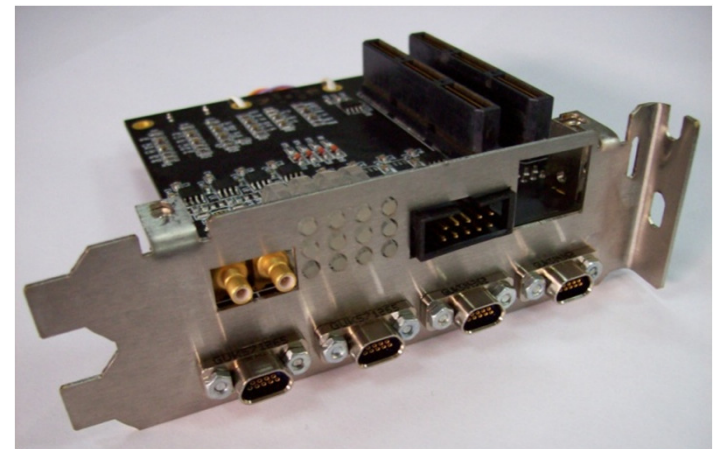
- Duration: February 2009 – April 2011 (active duration 18 months)
- Objectives
 - Requirements definition for Protocol Validation Tools for on-board communication networks
 - Provision of a PVS proof of concept prototype instance for
 - SpW/SpW-T protocols
 - SpW-D protocol
- Partners
 - TELETEL
 - ASTRIUM SAS (Toulouse)

PVS-Phase 1 Results

- Realisation of a high throughput 4-port SpW network interface board with high capacity FPGA and fine programmable transmission rates
- Implementation & validation of the draft SpW-T protocol specification in the board's FPGA logic
- Implementation of the GAMMA protocol emulator test cases and protocol decoder
- Design and implementation of SpW-D & RMAP IP cores in the SpW network interface board FPGA logic
- Implementation of RMAP/SpW-D test cases
- Protocols Experimentation/Validation and contributions to the SpW WG

Result List:

Test Case	Group	Verdict	Reason	Start Time	Stop Time	Duration	Start Date	Full Path
RMAP_TV_BH_000	TARGET_ERROR_HANDLER	PASS		16:11:26.579	16:11:27.512	933ms	17-03-2011	RMAP_TV_BH_000
RMAP_TV_BH_001	TARGET_ERROR_HANDLER	PASS		16:11:27.512	16:11:28.586	1074ms	17-03-2011	RMAP_TV_BH_001
RMAP_TV_BH_002	TARGET_ERROR_HANDLER	PASS		16:11:28.586	16:11:31.542	2956ms	17-03-2011	RMAP_TV_BH_002
RMAP_TV_BH_003	TARGET_ERROR_HANDLER	PASS		16:11:31.542	16:11:31.698	156ms	17-03-2011	RMAP_TV_BH_003
RMAP_TV_BH_004	TARGET_ERROR_HANDLER	PASS		16:11:31.698	16:11:31.995	297ms	17-03-2011	RMAP_TV_BH_004
RMAP_TV_BH_005	TARGET_ERROR_HANDLER	PASS		16:11:31.995	16:11:32.063	68ms	17-03-2011	RMAP_TV_BH_005
RMAP_TV_BH_006	TARGET_ERROR_HANDLER	PASS		16:11:32.063	16:11:32.438	375ms	17-03-2011	RMAP_TV_BH_006
RMAP_TV_BH_007	TARGET_ERROR_HANDLER	PASS		16:11:32.438	16:11:33.793	1355ms	17-03-2011	RMAP_TV_BH_007
RMAP_TV_BH_008	TARGET_ERROR_HANDLER	PASS		16:11:33.793	16:11:36.209	2416ms	17-03-2011	RMAP_TV_BH_008
RMAP_TV_BH_009	TARGET_ERROR_HANDLER	PASS		16:11:36.209	16:11:37.618	1409ms	17-03-2011	RMAP_TV_BH_009
RMAP_TV_BH_010	TARGET_ERROR_HANDLER	PASS		16:11:37.618	16:11:38.009	391ms	17-03-2011	RMAP_TV_BH_010
RMAP_TV_BH_011	TARGET_ERROR_HANDLER	PASS		16:11:38.009	16:11:40.408	2399ms	17-03-2011	RMAP_TV_BH_011
RMAP_TV_BH_012	TARGET_ERROR_HANDLER	PASS		16:11:40.408	16:11:41.833	1425ms	17-03-2011	RMAP_TV_BH_012
RMAP_TV_BH_013	TARGET_ERROR_HANDLER	PASS		16:11:41.833	16:11:42.268	435ms	17-03-2011	RMAP_TV_BH_013
RMAP_TV_BH_014	TARGET_ERROR_HANDLER	PASS		16:11:42.268	16:11:42.648	380ms	17-03-2011	RMAP_TV_BH_014
RMAP_TV_BH_015	TARGET_ERROR_HANDLER	PASS		16:11:42.648	16:11:43.095	447ms	17-03-2011	RMAP_TV_BH_015
RMAP_TV_BH_016	TARGET_ERROR_HANDLER	PASS		16:11:43.095	16:11:43.528	433ms	17-03-2011	RMAP_TV_BH_016
RMAP_TV_BH_017	TARGET_ERROR_HANDLER	PASS		16:11:43.528	16:11:43.930	402ms	17-03-2011	RMAP_TV_BH_017
RMAP_TV_BH_018	TARGET_ERROR_HANDLER	PASS		16:11:43.930	16:11:44.453	523ms	17-03-2011	RMAP_TV_BH_018
RMAP_TV_BH_019	TARGET_ERROR_HANDLER	PASS		16:11:44.453	16:11:44.868	415ms	17-03-2011	RMAP_TV_BH_019



PVS-Phase 2 Overview

■ Duration

- Period I: January 2012 – February 2013 (14 months)
- Period II: March 2013 – June 2014 (16 months)

■ Objectives

- To provide a protocol validation system for the SpW family of protocols (PVS SpW product)
- To provide a protocol validation system for the ECSS-E-ST-50-13C (MIL-STD-1553B) protocols (PVS 1553 product)
- To provide a protocol validation system for the ECSS-E-ST-50-15C (CANBUS) protocols (PVS CAN product)
- To validate the different PVS product instances in four (4) demonstrators

■ Partners

- TELETEL
- Astrium SAS (Toulouse, Elancourt)



PVS-P2 SpW activities (Period I)

- Extension of the SpW network Interface Board to support:
 - IRIG time-stamping for common time reference across different network technologies
 - Trigger I/F to allow synchronized traffic injection, “start of capture” events, etc.
 - UUT protection for connection to flight equipment
- Development of non-intrusive SpW monitoring functionality
 - Real-time Gbytes traffic capture
 - Filtering to support “information of interest” capture only
 - WireShark network analyzer protocol message decoders for RMAP, PTP/Space Packet
- Protocol emulators
 - RMAP
 - PTP/CCSDS Space Packet
 - API for custom applications development
- Protocol test suites
 - RMAP
 - PTP/CCSDS Space Packet

PVS-P2 1553 activities (Period I)

- Integration of COTS MIL-STD-1553 boards with SAFIRE-PVS
- Implementation of 1553 Bus Monitoring functionality
- Implementation of RT emulation functionality supporting ECSS-E-ST-50-13C
- Implementation of ECSS-E-ST-50-13C (1553) validation tests
- Integration of SAE RT test plans

PVS-P2 CAN activities (Period II)

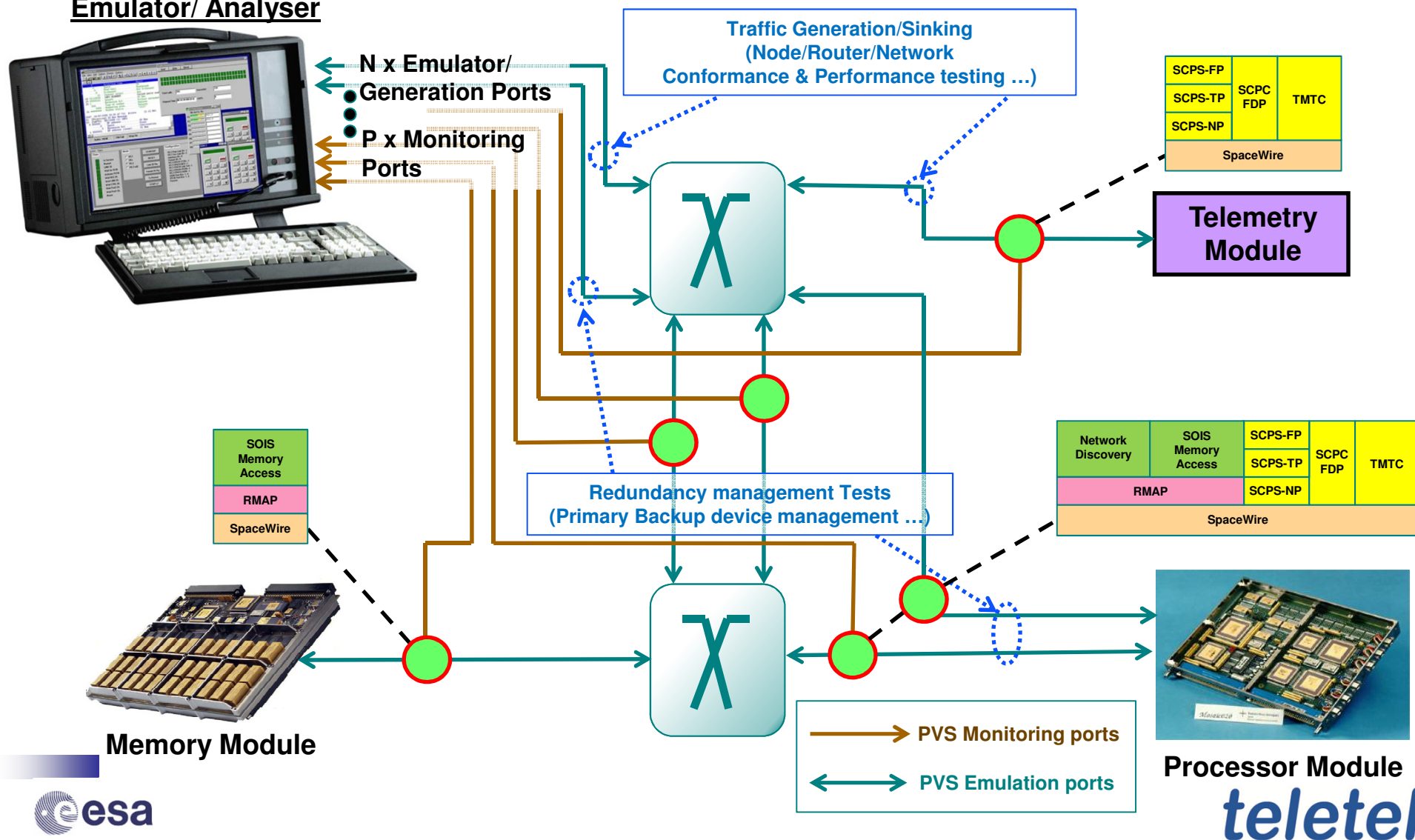
- Development of a ECSS-CAN network interface card with multiple ports, ISO/RS-485 PHYs, UUT protection, IRIG, Trigger I/F
- Implementation of CAN Bus Monitoring functionality
- Implementation of monitoring functionality for CAN, CANOpen and ECSS-E-50-15C messages
- Implementation of ECSS-E-50-15C (CAN) Slave node emulation functionality
- Implementation of CAN/CANopen Traffic Generation
- Implementation of ECSS-E- 50-15C (CAN) test suites

PVS-P2 Demonstration/Validation (Period II)

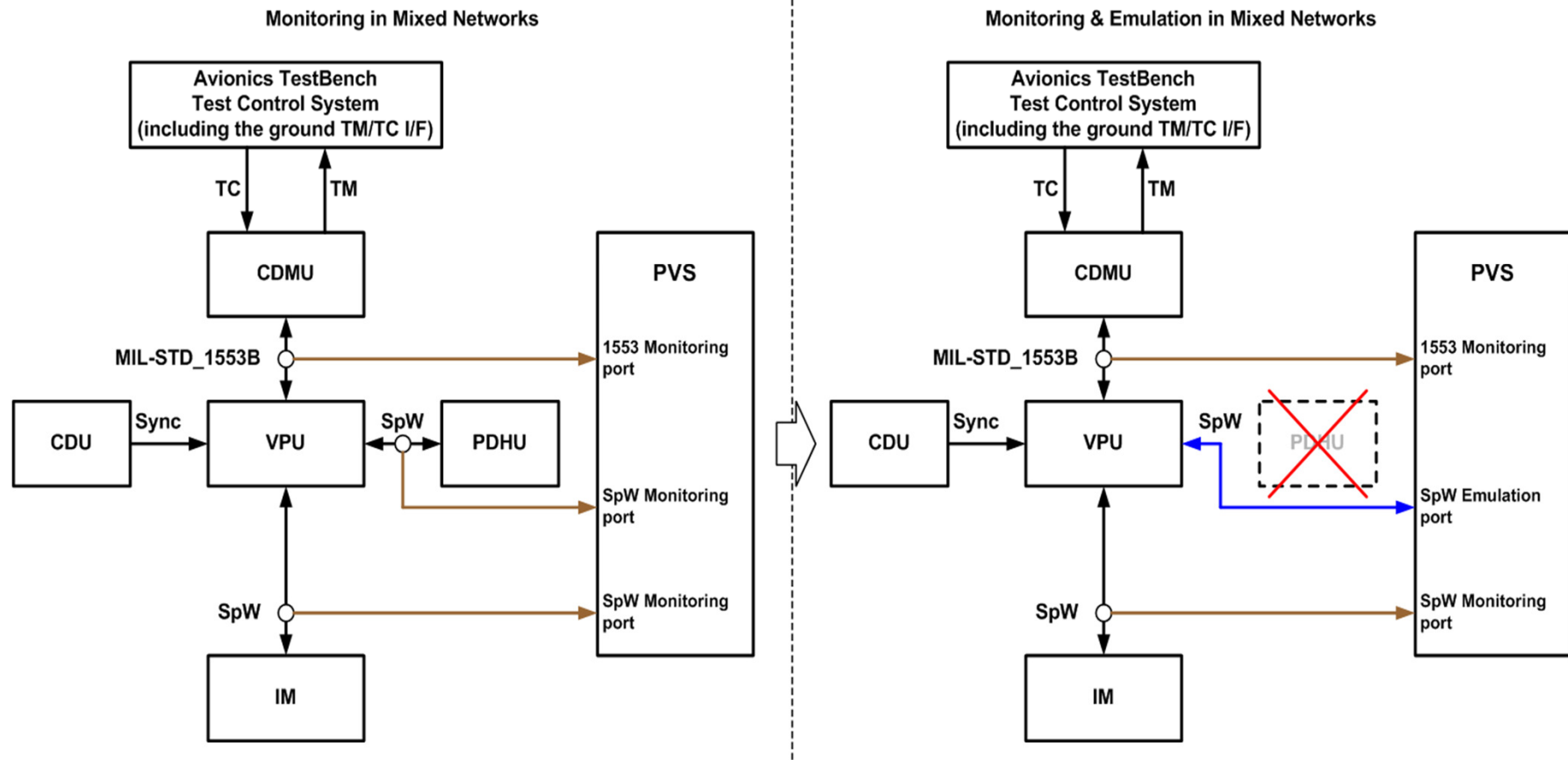
- PVS Demonstration with GAIA Video Processing Unit (VPU)
 - Monitoring on the SpW and 1553 networks with common (IRIG) time-stamping
 - PVS will emulate the PDHU and will simultaneously monitor the SpW and 1553 links
 - PVS will emulate the CDMU and will simultaneously monitor the SpW and 1553 links
- Solar Orbiter platform
 - Link Monitoring of OBC, SSMM
 - OBC and SSMM emulation
- PVS Demonstration with Telecom Sensor Networks (RTU)
 - Monitoring of an RTU's 1553 and CAN links
 - S/C Computer Unit and/or CAN Slave Node Emulation with simultaneous monitoring of the 1553 (SCU) and CAN (sensors) networks
- 1553 bus controller validation controlled by SpW link
 - Remote control of a RASTA board through the SpW Link, and validation of the traffic exchanged over the 1553 network

PVS: Protocol Emulator/Analyser

Usage Examples: SpW Scenarios



Usage Examples: Mixed network Scenarios



CDU: Clock Distribution Unit, VPU: Video Processing Unit, PDHU: Payload Data Handling Unit, CDMU: Clock & Data Management Unit, IM: Interconnection Module



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