



#### **SpaceWire Demonstrator**

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## <u>Agenda</u>

- Overview of Demonstration System
  - Instruments
  - Mass Memory Unit
  - Processor
  - Telemetry and Telecommand
  - RMAP
  - Network Configurations
- Demonstration





# The Demonstration System

- Simulation of onboard data handling network
- Four types of units:
  - Telemetry and Telecommand
  - Processor
  - Mass Memory Unit
  - Instruments
    - Camera Instrument
    - File Instrument
    - Pointer Instrument





### <u>Instruments</u>

- Three different Instrument types currently defined:
  - Camera Instrument
  - File Instrument
  - Pointer Instrument
- The maximum data rate used by each Instrument can be configured
- Future Instrument types can easily be created using the base classes which provide much of the functionality





## Camera Instrument

- Sends images generated by a camera to the MMU
- Can be set to send all images recorded by the camera
- Or can be commanded to send a single image
- The images can be displayed at the TM/TC





### Camera Instrument







# File Instrument

- Sends files read from a directory to the MMU
- Can be set to send all files in the directory
- Or can be commanded to send a single file
- If the files contain images, these can be displayed at the TM/TC unit





# Pointer Instrument

- Sends mouse pointer position information to the MMU
- Can be set to send all mouse movements
- Or can be commanded to send a single position
- The mouse position can be displayed at the TM/TC unit
- Different from the other two Instruments as the data is not sent at a steady rate





# Mass Memory Unit

- Allows channels to be created to store data from Instruments
- Can be commanded by the Processor to transfer data from the channels to the TM/TC
- Graphically represents the amount of data in each channel





### Mass Memory Unit

		+ st	AR-Dund	lee
Channel 0	0%	50%		Channel: 0 Source Address: 67 Size: 40960 In Use: 86% Status: Not Sending
	(	Percentage View Byte View	Configure Ch	annels





#### <u>Processor</u>

- Can execute commands, e.g.:
  - Set-up routing tables
  - Configure Instruments
  - Configure MMU channels
  - Enable an MMU
  - Transfer data from an MMU
- May be instructed by TM/TC to execute these commands
- Read the status of the devices on the network





#### <u>Processor</u>

👙 Processor						
STAR-Dundee						
Configure MMU Enable MMU Transfer	Create New Script					
Disable MMU Transfer	Create New Time Tagged Script					
Enable Instrument	Edit Script					
Set Routing Tables	Delete Script					
Turron www	Run Script					
'						
Processor address: 65 TM/TC address: 64						
Load Configuration	Specify Network Topology					
	Exit					
Status: Script successfully completed						





## **Telemetry and Telecommand**

- Specify the Processors to communicate with
- Switch between prime and redundant configurations
- Issue commands to be executed at the Processor
- Upload commands to the Processor
- View the data transmitted from the MMU
- Display the status of the devices on the network





### **Telemetry and Telecommand**

🍰 Telemetry and Telecommand						
STAR-Dundee						
Issue Command Create Time Tagged Command						
No Error						
Specify Processor Addresses Show Network Status						
Switch Network Configuration Load Configuration						
View Instrument Data Save Configuration						
Exit						





# Remote Memory Access Protocol

- RMAP used for all communication
  - Read and write commands used
  - Incremental and non-incremental operations
  - Acknowledged and unacknowledged writes
  - MMU uses extended address field to indicate the channel number
- Latest draft (Draft F) with updated CRC used





## **Network Configurations**



- Basic System
  - No redundancy
  - One of each component
  - All exchanges through the router



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## **Network Configurations**



- System with redundancy
  - Redundant MMU/Processor/router and TM/TC blocks



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**Network Configurations** 



- Multiple Instrument System
  - No redundancy of components
  - High rate Instrument connected directly to the MMU/Processor router
  - Multiple links to Processor and MMU





### **Demonstration**