



# The new SpaceWire compliant SMCS332SpW / SMCS116Spw ASICs

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## Status

SpaceWire Working Group 18. + 19. May 2006

- **Prototype Approval given**
- **Chip uses SpW Core version 1.4**
  - detailed description about deviation from SpW standard due to the core will be given in User Manual**
- **SEU free cells used**
- **Atmel part number: AT7901F**

- **New SpW Core 2.0 will be used**
- **Chip fully SpaceWire compliant**
- **Chip can use STUP protocol**
  - > **Chip compliant to SpW protocol**
- **SEU free cells used**
- **Atmel part number: AT7906F**

## SMCS332SpW

- Radiation tolerant gate array technology from Atmel: MG2RT (0,5  $\mu\text{m}$ )
- 196 pins
- power consumption: 1,7 W
- 3.3V version: 100MBit/s
- 5 V version: 200MBit/s

## SMCS116SpW

- Radiation tolerant gate array technology from Atmel: MG2RT (0,5  $\mu\text{m}$ )
- 100 pins
- power consumption: TBC
- 3.3V version: 100MBit/s
- 5 V version: 200MBit/s

## SMCS332SpW

- prototypes tested
- Prototype approval given
- ASIC listed in Atmel catalog,  
available after User Manual update.

## SMCS116SpW

- Layout phase
- CDR (Design Review) planned in August 06
- Prototype approval planned in October 06