SpaceWire RMAP checksum calculation

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Checksum type?

- CRC or longitudinal parity?
- Remember that SpaceWire already has byte parity!
- If CRC selected the algorithm <u>and</u> implementation must be specified
- Polynomial: $g(x) = x^8 + x^2 + x^1 + 1$

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Fibonacci implementation



• Forward CRC

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Galois implementation



- Reverse CRC
- Produces zero result if a checksum is summed with itself
- Note the byte order numbering based on bit 0 (LSB) entered first

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Galois implementation, VHDL code

```
-- Purpose : Generate CRC checksum function
-- A is the input byte
-- StartValue is the accumulated CRC checksum
 ______
function CRC8(A : Byte T; StartValue : Byte T) return Byte T is
  variable NextStart : Byte T;
  variable CRCloop : std ulogic;
 begin
   NextStart := StartValue;
   for I in 0 to 7 loop -- For serial transfer with LSB first(SPW)
     CRCloop := NextStart(0) xor A(I);
     NextStart := CRCloop &
                  NextStart(7) xor CRCloop &
                  NextStart(6) xor CRCloop &
                  NextStart(5 downto 1);
   end loop;
   return NextStart; -- CRC checksum
 end function:
```

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